

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S12 2	499	(EDA electronic adj design adj automation) same circuit same simulat\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/31 16:19
S12 3	6	(EDA electronic adj design adj automation) same circuit same simulat\$3 same timing adj data	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/31 16:20
S12 4	1334	345/440.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 09:41
S12 5	308	702/67.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 09:44
S12 6	115	702/68.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 09:46
S12 7	346	703/19.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 09:50
S12 8	2	"6662126".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 09:50

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S12 9	2	"6530065".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 13:31
S13 0	5	(("6,453,450") or ("5,987,246") or ("4,812,996") or ("6,332,212") or ("6,173,438")).PN.	US-PGPUB; USPAT	OR	OFF	2008/02/01 14:49
S13 1	23	(timing adj diagram waveform) with (altered adjusted changed) with user same circuit with (modified changed adjusted altered)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 14:03
S13 2	2	("5995736" "6530054").PN. OR ("7051299").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 14:02
S13 3	5620	(timing adj diagram waveform) with (altered adjusted changed) with user circuit with design with (modified changed adjusted altered)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 14:03
S13 4	5	(timing adj diagram waveform) with (altered adjusted changed) with user and circuit with design with (modified changed adjusted altered)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 14:03
S13 5	55	(timing adj diagram waveform) with user and circuit with design with (modified changed adjusted altered)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 14:04
S13 6	1	(timing adj diagram waveform) with user and circuit with design with (modified changed adjusted altered) with automatic\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 14:04

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S13 7	5	(timing adj diagram waveform) with (modified changed adjusted altered) with user and circuit with design with (modified changed adjusted altered)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 14:05
S13 8	11	(timing adj diagram waveform) with (modified changed adjusted altered) with user and circuit with design	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 14:06
S13 9	128	(timing adj diagram waveform) with (alter\$3 modify\$3 chang\$3 adjust\$3 alter\$3) and EDA	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 14:06
S14 0	77	(timing adj diagram waveform) with (alter\$3 modify\$3 chang\$3 adjust\$3 alter\$3) and EDA and circuit with design	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 14:06
S14 1	8	(timing adj diagram waveform) with (alter\$3 modify\$3 chang\$3 adjust\$3 alter\$3) same EDA and circuit with design	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 14:06
S14 2	6	("5371851" "5740086" "6006022" "6651204" "6668331").PN. OR ("7089517").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2008/02/01 14:33
S14 3	124	(waveform timing adj diagram) same (alter\$3 adjust\$3 chang\$3 modif\$7) same circuit and EDA	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 14:45

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S14 4	32	(waveform timing adj diagram) same (alter\$3 adjust\$3 chang\$3 modif\$7) same circuit with design and EDA	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 14:47
S14 5	25	(waveform timing adj diagram) with (alter\$3 adjust\$3 chang\$3 modif\$7) same circuit with design and EDA	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 14:47
S14 6	34	edit\$3 with (timing adj diagram waveform) same circuit with (design simulation)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 17:44
S14 7	0	select\$3 with (node probe) with circuit and (waveform timing adj diagram) with edit\$3 same updat\$3 with (design layout)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 17:46
S14 8	1	(node probe) with circuit and (waveform timing adj diagram) with edit\$3 same updat\$3 with (design layout)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 17:47
S14 9	4	(node probe) with circuit and (waveform timing adj diagram) with edit\$3 and updat\$3 with (design layout)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 17:47
S15 0	2	(waveform timing adj diagram) with edit\$3 and updat\$3 with (design layout) with circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 17:48

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S15 1	116	(waveform timing adj diagram) with edit\$3 and (design layout) with circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 17:48
S15 2	18	(waveform timing adj diagram) with edit\$3 and (design layout) with circuit and EDA	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 18:04
S15 3	88	waveform adj editor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 18:05
S15 4	5	waveform adj editor and EDA	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 18:06
S15 5	20	waveform adj editor and FPGA	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/01 18:06
S15 6	1	("6530065").PN.	US-PGPUB; USPAT	OR	OFF	2008/02/02 13:33